

Amendments to the Specification

Change page 6, lines 16-26, as follows:

Further, the control device may be provided with a first judging means for judging whether the data written to the memory means are correct, and a second judging means for judging whether the writing means write writes the data to the memory means correctly. Accordingly, when the data in the memory region of the memory means are replaced with the data of the external memory medium, it is possible to confirm that the operation control program data are written correctly after the writing process and that the writing process is completed normally, thereby controlling the electronic device using only the correct operation control programs.

Change page ~~6~~¹¹, lines 23-29, as follows:

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As shown in FIG. 3, the EPROM 32 34 has three memory regions, i.e. area D, area E and area F, similar to the EEPROM 32. The area D is a memory region for storing the same type of data as in the area A in the EEPROM 32. The data are used for determining the connection of the EEPROM 32 (described later) and the compatibility of the apparatus to the control program for writing stored in the area E.

Change page 9, lines 19-28, as follows:

The control unit 30 is formed of a single chip computer with a peripheral LSI and a memory formed therein. The control unit 30 is equipped with a CPU 31 for controlling the device to be controlled; an EEPROM 31 32 (memory that electrically writes a control program) for storing various control programs for executing operations; and a RAM 33 for temporarily storing data generated during a process in the CPU 31 and a signal from a plurality of sensors 38 or an image